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1 Placement: Accuracy driven performance macromodeling of feasible regions during synthesis of analog circuits

Anuradha Agarwal, Glenn Wolfe, Ranga Vemuri

April 2005 **Proceedings of the 15th ACM Great Lakes symposium on VLSI**

Publisher: ACM Press

Full text available: pdf(109.72 KB) Additional Information: full citation, abstract, references, index terms

We propose an accuracy driven synthesis methodology for analog circuits. The proposed approach relies on macro-models for performance estimation and is thus orders of magnitude faster than simulation based synthesis techniques. Unlike existing macro-model based approaches, which use static models, our approach dynamically improves the accuracy of the model during synthesis to ensure true convergence. Our method is based on identifying and accurately modeling those regions in the design space whe ...

Keywords: analog synthesis, circuit sizing, performance modeling

2 An analog performance estimator for improving the effectiveness of CMOS analog systems circuit synthesis

Adrian Nunez-Aldana, Ranga Vemuri

January 1999 **Proceedings of the conference on Design, automation and test in Europe**

Publisher: ACM Press

Full text available: pdf(108.77 KB) Additional Information: full citation, citings, index terms

3 H/S Embedded Systems: Performance analysis with confidence intervals for embedded software processes

Per Bjuréus, Axel Jantsch

September 2001 **Proceedings of the 14th international symposium on Systems synthesis**

Publisher: ACM Press

Full text available: pdf(269.12 KB) Additional Information: full citation, abstract, references, citings, index terms

The choice of algorithms has a large impact on the performance of embedded real-time systems. Therefore, performance estimation of embedded software is vital in an early

design phase. Consequently, high-level estimation techniques have been devised, but the accuracy of the estimations vary a lot depending on the algorithm and its context. We address this problem by proposing an estimation technique that both estimates the performance and computes the expected accuracy. The accuracy is used to pr ...

4 Rendering: 3D graphics rendering time modeling and control for mobile terminals

Nicolaas Tack, Francisco Morán, Gauthier Lafruit, Rudy Lauwereins

April 2004 **Proceedings of the ninth international conference on 3D Web technology**

Publisher: ACM Press

Full text available: pdf(348.33 KB) Additional Information: full citation, abstract, references, index terms

3D graphics has found its way to mobile devices such as Personal Digital Assistants (PDA) and cellular phones. Given their limited battery capabilities, these devices typically have less computational resources available than their counterparts connected to a power supply. Additionally, the workload of 3D graphics applications changes very drastically over time. These different and changing conditions make the creation of 3D content a real challenge for the content creators.To allow the renderin ...

Keywords: MPEG-4 WSS, mobile terminals, rendering time control, rendering time modeling

5 A two-layer library-based approach to synthesis of analog systems from VHDL-AMS specifications

Alex Doboli, Nagu Dhanwada, Adrian Nunez-Aldana, Ranga Vemuri

April 2004 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 9 Issue 2

Publisher: ACM Press

Full text available: pdf(658.00 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a synthesis methodology for analog systems described using VHDL-AMS language. Synthesis produces net-lists of analog components that are selected from a library, and sized so that specified objectives (like AC response, signal to noise ratio, dynamic range, area) are optimized. The gap between abstract specifications and implementations is bridged using a two-layered methodology. The first layer is architecture generation. The second layer is component synthesis and constrain ...

Keywords: Analog synthesis, VHDL-AMS, branch-and-bound, genetic algorithms, performance estimation

6 Advances in analog circuit and layout synthesis: Fast and accurate parasitic capacitance models for layout-aware

Anuradha Agarwal, Hemanth Sampath, Veena Yelamanchili, Ranga Vemuri

June 2004 **Proceedings of the 41st annual conference on Design automation**

Publisher: ACM Press

Full text available: pdf(121.59 KB) Additional Information: full citation, abstract, references, index terms

Considering layout effects early in the analog design process is becoming increasingly important. We propose techniques for estimating parasitic capacitances based on look-up tables and multi-variate linear interpolation. These models enable fast and accurate estimation of parasitic capacitances and are very suitable for use in a synthesis flow. A layout aware methodology for synthesis of analog CMOS circuits using these parasitic models is presented. Results indicate that the proposed synthesis ...

Keywords: analog synthesis, layout aware, parasitic estimation

7 Behavioral synthesis of analog systems using two-layered design space exploration
Alex Doboli, Adrian Nunez-Aldana, Nagu Dhanwada, Sree Ganesan, Ranga Vemuri
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**
Publisher: ACM Press
Full text available: pdf(165.35 KB) Additional Information: full citation, references, citings, index terms

8 Distributed stochastic discrete-event simulation in parallel time streams
Krzysztof Pawlikowski, Victor W. C. Yau, Don McNickle
December 1994 **Proceedings of the 26th conference on Winter simulation**
Publisher: Society for Computer Simulation International
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9 Functional partitioning improvements over structural partitioning for packaging constraints and synthesis: tool performance
Frank Vahid, Thuy Dm Le, Yu-Chin Hsu
April 1998 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 3 Issue 2
Publisher: ACM Press
Full text available: pdf(225.74 KB) Additional Information: full citation, abstract, references, citings, index terms

Incorporating functional partitioning into a synthesis methodology leads to several important advantages. In functional partitioning, we first partition a functional specification into smaller subspecifications and then synthesize structure for each, in contrast to the current approach of first synthesizing structure for the entire specification and then partitioning that structure. One advantage is the improvement in I/O performance and package count, when partitioning among hardware block ...

Keywords: behavioral synthesis, functional partitioning, system-level design

10 Modeling methodology for integrated simulation of embedded systems
Akos Ledeczi, James Davis, Sandeep Neema, Aditya Agrawal
January 2003 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 13 Issue 1
Publisher: ACM Press
Full text available: pdf(951.86 KB) Additional Information: full citation, abstract, references, citings, index terms

Developing a single embedded application involves a multitude of different development tools including several different simulators. Most tools use different abstractions, have their own formalisms to represent the system under development, utilize different input and output data formats, and have their own semantics. A unified environment that allows capturing the system in one place and one that drives all necessary simulators and analysis tools from this shared representation needs a common r ...

Keywords: Simulation, domain specific languages, metamodeling, model integrated computing, modeling, simulation integration

11 Comparing the performance of collection selection algorithms
Allison L. Powell, James C. French

October 2003 **ACM Transactions on Information Systems (TOIS)**, Volume 21 Issue 4
Publisher: ACM Press

Full text available: pdf(668.40 KB) Additional Information: full citation, abstract, references, citings, index terms

The proliferation of online information resources increases the importance of effective and efficient information retrieval in a multicollection environment. Multicollection searching is cast in three parts: collection selection (also referred to as database selection), query processing and results merging. In this work, we focus our attention on the evaluation of the first step, collection selection.In this article, we present a detailed discussion of the methodology that we used to evaluate an ...

Keywords: Collection selection, database selection, distributed information retrieval, distributed text retrieval, metasearch engine, resource discovery, resource ranking, resource selection, server ranking, server selection, text retrieval

12 Rapid Configuration and Instruction Selection for an ASIP: A Case Study
Newton Cheung, Jorg Henkel, Sri Parameswaran
March 2003 **Proceedings of the conference on Design, Automation and Test in Europe - Volume 1 DATE '03**
Publisher: IEEE Computer Society
Full text available: pdf(447.59 KB) Publisher Site Additional Information: full citation, abstract, index terms

We present a methodology that maximizes the performance of Tensilica based Application Specific Instruction-set Processor (ASIP) through instruction selection when an area constraint is given. Our approach rapidly selects from a set of pre-fabricated coprocessors/functional units from our library of pre-designed specific instructions (to evaluate our technology we use the Tensilica platform). As a result, we significantly increase application performance while area constraints are satisfied. Our ...

13 Efficient and accurate cost models for parallel query optimization (extended abstract)
Sumit Ganguly, Akshay Goel, Avi Silberschatz
June 1996 **Proceedings of the fifteenth ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems**
Publisher: ACM Press
Full text available: pdf(1.06 MB) Additional Information: full citation, references, citings, index terms

14 Automating parallel simulation using parallel time streams
Victor Yau
April 1999 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 9 Issue 2
Publisher: ACM Press
Full text available: pdf(194.69 KB) Additional Information: full citation, abstract, references, index terms

This paper describes a package for parallel steady-state stochastic simulation that was designed to overcome problems caused by long simulation times experienced in our ongoing research in performance evaluation of high-speed and integrated-services communication networks, while maintaining basic statistical rigors of proper analysis of simulation output data. The package, named AKAROA, accepts ordinary (nonparallel) simulation programs, and alll further stages of stochastic simulation shou ...

Keywords: distributed simulation, interprocess communication, output analysis methodology, parallel processing, parallel simulation, parallel time streams, spectral

analysis, speedup

15 Co-design architecture and synthesis: Compiler-directed customization of ASIP cores
T. Vinod Kumar Gupta, Roberto E. Ko, Rajeev Barua
May 2002 **Proceedings of the tenth international symposium on Hardware/software codesign**
Publisher: ACM Press
Full text available: pdf(629.06 KB) Additional Information: full citation, abstract, references, index terms

This paper presents an automatic method to customize embedded application-specific instruction processors (ASIPs) based on compiler analysis. ASIPs, also known as embedded soft cores, allow certain hardware parameters in the processor to be customized for a specific application domain. They offer low design cost as they use pre-designed and verified components. Our design goal is choosing parameter values for fastest runtime within a given silicon area budget for a particular application set. Pr ...

Keywords: ASIP, customization, embedded, soft cores

16 Business process modeling/reengineering: Customer relations management: service operations: using simulation to approximate subgradients of convex performance measures in service systems
Júlíus Atlason, Marina A. Epelman, Shane G. Henderson
December 2003 **Proceedings of the 35th conference on Winter simulation: driving innovation**
Publisher: Winter Simulation Conference
Full text available: pdf(321.13 KB) Additional Information: full citation, abstract, references

We study the problem of approximating a subgradient of a convex (or concave) discrete function that is evaluated via simulation. This problem arises, for instance, in optimization problems such as finding the minimal cost staff schedule in a call center subject to a service level constraint. There, subgradient information can be used to significantly reduce the search space. The problem of approximating subgradients is closely related to the one of approximating gradients and we suggest and c ...

17 Analysis methodology: Panel discussion on current issues in input modeling: panel on current issues in simulation input modeling
Russell R. Barton, Stephen E. Chick, Russell C. H. Cheng, Shane G. Henderson, Averill M. Law, Bruce W. Schmeiser, Lawrence M. Leemis, Lee W. Schruben, James R. Wilson
December 2002 **Proceedings of the 34th conference on Winter simulation: exploring new frontiers**
Publisher: Winter Simulation Conference
Full text available: pdf(319.82 KB) Additional Information: full citation, abstract, references, citings

In recent years, substantial progress has been made in the development of powerful new approaches to modeling and generation of the stochastic input processes driving simulation models. In this panel discussion, we examine some of the central issues and unresolved problems associated with each of these approaches to simulation input modeling.

18 Hierarchical constraint transformation using directed interval search for analog system synthesis
Nagu R. Dhanwada, Adrian Nunez-Aldana, Ranga Vemuri

January 1999 **Proceedings of the conference on Design, automation and test in Europe**
Publisher: ACM Press
Full text available: pdf(259.78 KB) Additional Information: full citation, citings, index terms

19 Research sessions: continuous queries and streams: Rate-based query optimization for streaming information sources
Stratis D. Viglas, Jeffrey F. Naughton
June 2002 **Proceedings of the 2002 ACM SIGMOD international conference on Management of data**
Publisher: ACM Press
Full text available: pdf(1.11 MB) Additional Information: full citation, abstract, references, citings, index terms

Relational query optimizers have traditionally relied upon table cardinalities when estimating the cost of the query plans they consider. While this approach has been and continues to be successful, the advent of the Internet and the need to execute queries over streaming sources requires a different approach, since for streaming inputs the cardinality may not be known or may not even be knowable (as is the case for an unbounded stream.) In view of this, we propose shifting from a cardinality-ba ...

20 Research session: DB and IR #1: An efficient and versatile query engine for TopX search
Martin Theobald, Ralf Schenkel, Gerhard Weikum
August 2005 **Proceedings of the 31st international conference on Very large data bases VLDB '05**
Publisher: VLDB Endowment
Full text available: pdf(442.21 KB) Additional Information: full citation, abstract, references, index terms

This paper presents a novel engine, coined *TopX*, for efficient ranked retrieval of XML documents over semistructured but nonschematic data collections. The algorithm follows the paradigm of threshold algorithms for top-k query processing with a focus on inexpensive sequential accesses to index lists and only a few judiciously scheduled random accesses. The difficulties in applying the existing top-k algorithms to XML data lie in 1) the need to consider scores for XML elements while aggreg ...

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